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Notice of Allowability	Application No.	Applicant(s)	
	10/086,155	KATOH, MASAOKI	
	Examiner	Art Unit	
	Shouxiang Hu	2811	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 4/20/2006.
2. ☒ The allowed claim(s) is/are 1,8,10 and 26.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

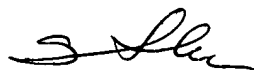
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input checked="" type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date <u>6/1/2006</u> . |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____ |


SHOUXIANG HU
PRIMARY EXAMINER

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Ilya Chorny (RN: 56,519) on June 1, 2006.

The application has been amended as follows:

IN THE CLAIMS

Claim 1: (currently amended) A light-emitting diode comprising a light-emitting diode chip mounted on a surface of a printed substrate, the light-emitting diode chip including:

a transparent substrate;

a semiconductor layer laminated on the transparent substrate and formed of an N-type semiconductor layer and a P-type semiconductor layer, wherein a PN junction interface between the N-type and P-type semiconductor layers is perpendicular to the surface of the printed substrate, the semiconductor layer including:

a light-emitting portion in the vicinity of the PN junction interface; and

a first face and a second face, both the first face and the second face parallel to the transparent substrate wherein the first face is in contact with the transparent substrate;

a first corner and a second corner adjacent to the surface of the printed substrate;

a pair of electrodes for applying voltage to the semiconductor layer;

a light reflecting layer formed of a metal thin film formed on the second face of the semiconductor layer substantially parallel to the PN junction interface and for reflecting light emitted from the light-emitting portion; and

a recessed section formed only at a- the first corner of the semiconductor layer adjacent to the printed substrate, the recessed section formed ~~through~~ by removing a portion of the light reflecting layer and a portion of the semiconductor layer to form an exposed surface of one of the N-type semiconductor layer and the P-type semiconductor layer;

wherein a first electrode of the pair of electrodes is formed only at the second corner on the light reflecting layer and a second electrode of the pair of electrodes is formed on the exposed surface adjacent to the printed substrate.

Claim 2-7 (cancelled)

Claim 8 (previously presented): A light-emitting diode claimed in Claim 1, wherein the metal thin film has a thickness of 100 nm or more.

Claim 9 (cancelled)

Claim 10 (previously presented): A light-emitting diode claimed in Claim 1, wherein the transparent substrate is transparent to color emitted by the light-emitting diode chip.

Claim 11 -25 (cancelled)

Claim 26 (previously presented): A light-emitting diode claimed in Claim 8, wherein the transparent substrate is transparent to color emitted by the light-emitting diode chip.

Claim 27 (cancelled)

Allowable Subject Matter

Claims 1, 8, 10 and 26 are allowed.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Shouxiang Hu whose telephone number is 571-272-1654. The examiner can normally be reached on Monday through Thursday, 7:30 AM to 6:00 PM.

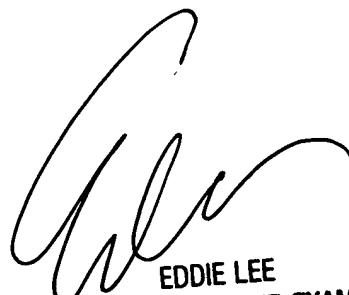
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on 571-272-1732. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

SH
June 5, 2006



SHOUXIANG HU
PRIMARY EXAMINER



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